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Inhee Lee is an assistant professor in the Electrical and Computer Engineering department at the University of Pittsburgh. Inhee Lee received his B.S. and M.S. degrees in electrical and electronic engineering from Yonsei University, South Korea, in 2006 and 2008, respectively, and a Ph.D. degree in electrical engineering from the University of Michigan in 2014. He was an assistant research scientist at the University of Michigan from 2015 to 2019. Currently, he is leading PITT Circuit Lab developing adaptive, energy-efficient reference circuits, sensor interfaces, energy harvesters, power management circuits, machine learning accelerators, emerging memory interfaces, and Cryogenic-CMOS circuits. Also, he is developing low-power millimeter-scale or even smaller sensing/computing systems for ecological, biomedical, energy exploration, and internet-of-things applications. Dr. Lee is an IEEE senior member and has been serving as a Technical Program Committee (TPC) Member for IEEE International Solid-State Circuits Conference (ISSCC), IEEE Symposium on VLSI Technology and Circuits (VLSI), IEEE Custom Integrated Circuits Conference (CICC), IEEE Asian Solid-State Circuits Conference (ASSCC), IEEE Circuits & Systems (CAS) Analog Signal Processing Technical Committee (ASPTC) and ACM/IEEE International Symposium on Low Power Electronics and Design (ISLPED).